



Attorney's Docket No. 5646-42DVIP

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Mario Au et al.  
Serial No.: 10/612,849  
Filed: July 3, 2003

Group Art Unit: 2186  
Examiner: To Be Assigned  
Confirmation No.: 6889

For: FIFO MEMORY DEVICES HAVING MULTI-PORT CACHE MEMORY ARRAYS  
THEREIN THAT SUPPORT HIDDEN EDC LATENCY AND BUS MATCHING AND  
METHODS OF OPERATING SAME

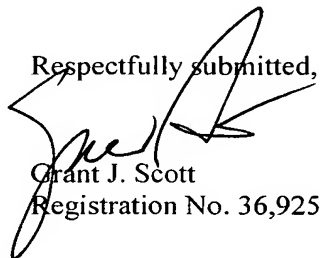
Date: October 21, 2003

Mail Stop PG Pub  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**STATEMENT OF IMPROPER CERTIFICATION**

The above-identified application was filed as a continuation-in-part (CIP) of U.S. Application Serial No. 10/307,638, filed December 2, 2002, which is a divisional of U.S. Application Serial No. 09/721,478, which issued on April 8, 2003 as U.S. Patent No. 6,546,461 (the "parent application"), and included a request for nonpublication and certification as set forth on form PTO/SB/35. However, this parent application, which was filed in the United States on November 22, 2000, was filed as a foreign application. Because of this earlier filed foreign application, Applicants believe the certification made in the original request for nonpublication was improper when the present application was filed. Accordingly, Applicants hereby notify the Patent Office of this improper certification, which was made based on an incorrect reading of the statutory provisions relating to publication and without any deceptive intent.

Respectfully submitted,

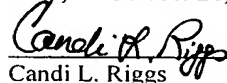


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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop PG Pub, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 21, 2003.



Candi L. Riggs

Date of Signature: October 21, 2003